



[54] METHOD OF RETRIEVING AND STORING COMPUTER PERIPHERAL DATA

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[\*] Notice: This patent is subject to a terminal disclaimer.

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[58] Field of Search ..... 364/200; 395/200, 395/DIG. 1; 709/217

References Cited

U.S. PATENT DOCUMENTS

4.783.730	11/1988	Fischer	364/200
4.864.532	9/1989	Reeve et al.	364/900
5.471.634	11/1995	Giorgio et al.	395/200.01

OTHER PUBLICATIONS

ANSI X3.131-1986. pp. 26, 51-71, 80-82, 185-186, 194-199, 208-209.

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[57] ABSTRACT

An improved method of having an SCSI controller provide a logical connection between a plurality of host processors using a single SCSI initiator. The controller contains removable host adapters for connection to any type of host processor interface. When a host processor requests status information of a target device, that status information is passed to the requesting processor and subsequently stored in the controller and made available to all other host processors. The advantage to such a controller is the expanded number of host processor connections to a single SCSI initiator whereby each host processor can independently and logically connect through the controller and SCSI initiator to a target without clearing target status information for other host processors. The invention is particularly advantageous when adapted to function in a local area network (LAN) arrangement where a significant number of host processors are requesting data from one mass storage device.

5 Claims, 4 Drawing Sheets

